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EVOLVING STORAGE NEEDS

Data streaming from Instruments

Traditional HPC
Modeling & Simulation

Data Science Analytics

Analysis, Search & Compare

Artificial Intelligence Decision making



https://www.orau.gov/ssioworkshop2018/agenda.htm

EVOLVING STORAGE TECHNOLOGIES



Storage Class Memory:

- Persistent, like storage
- Byte-addressable, like memory
- Lower latency, higher BW, greater endurance than Flash
- Creates a new storage tier between DRAM and NAND SSDs

Challenge: exploit SCM for evolving storage workloads.



HIGH PERFORMANCE STORAGE EVOLUTION

HPC Before 2016

Memory

Parallel File System HDD

Archive

HPC After 2016

Memory

Burst Buffer SSD

Parallel File System HDD

Archive

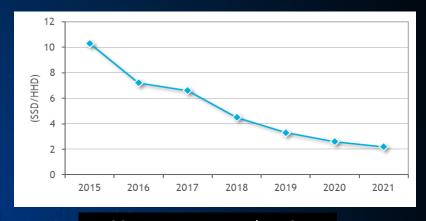
HPC Post 2019

Memory

Performance Tier SCM + SSD

Capacity Tier SSD or HDD

Archive

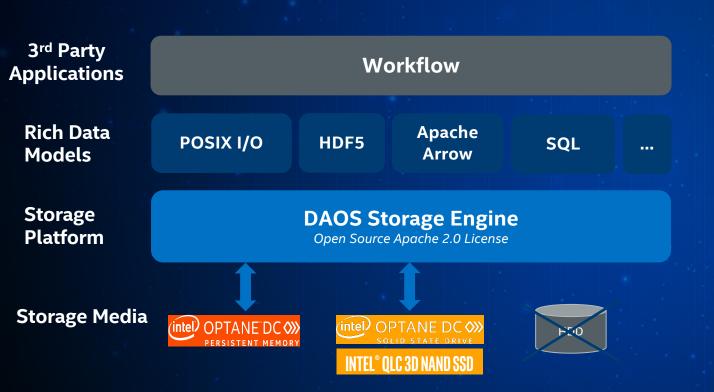


SSD vs HDD Pricing (per GB ratio)

Source: Hyperion Resources, IDC, Stifel 2018



<u>DISTRIBUTED ASYNCHRONOUS OBJECT STORAGE</u>



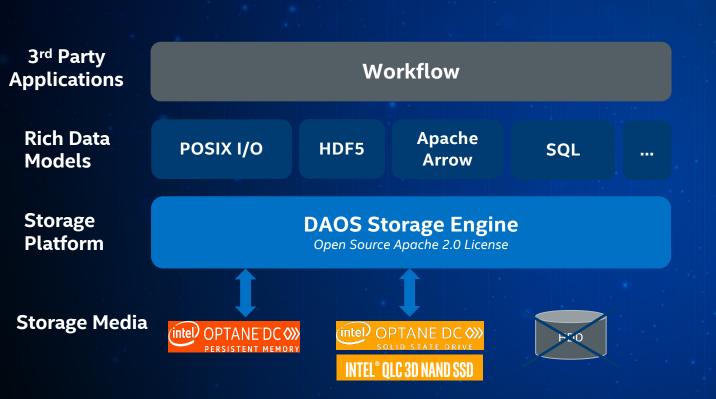
Benefits

- Built natively over new userspace PMEM/NVMe software stack
- Rich storage semantics
- High throughput/IOPS @arbitrary alignment/size
- Fine-grained, low-latency & True zero-copy I/Os
- Scalable communications
- Software-managed redundancy
- Rely on COTS hardware



DISTRIBUTED ASYNCHRONOUS OBJECT STORAGE

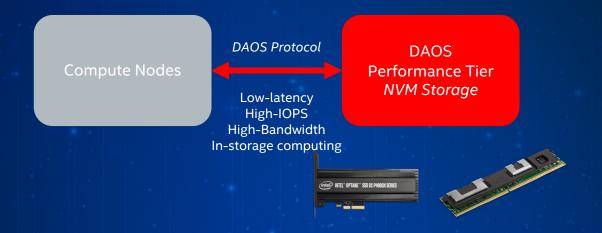




Benefits

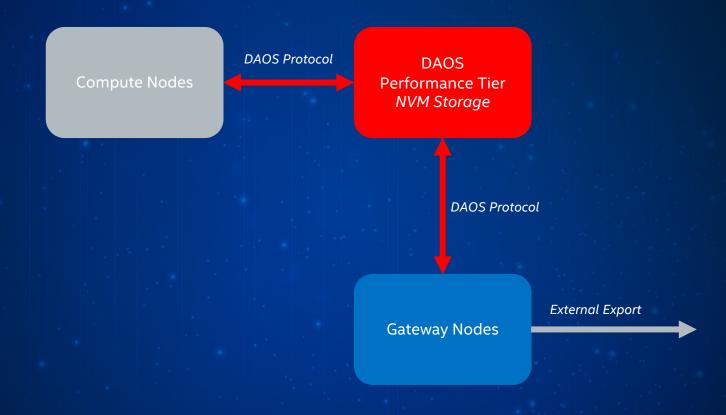
- Built natively over
- High three solutions was standard alignment of latency & latency &
 - - -managed redundancy
 - on **COTS** hardware

STORAGE ARCHITECTURE



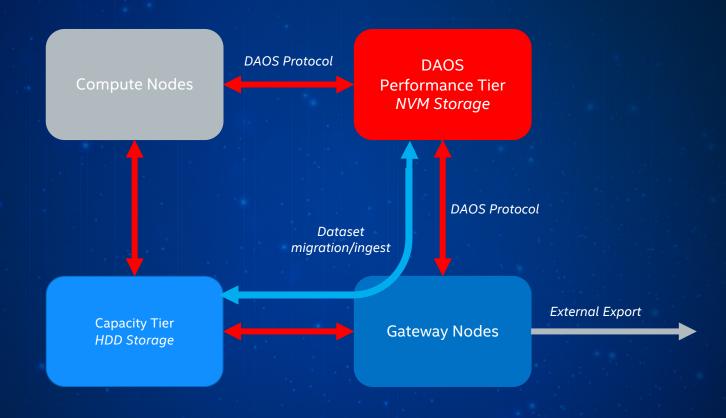


STORAGE ARCHITECTURE



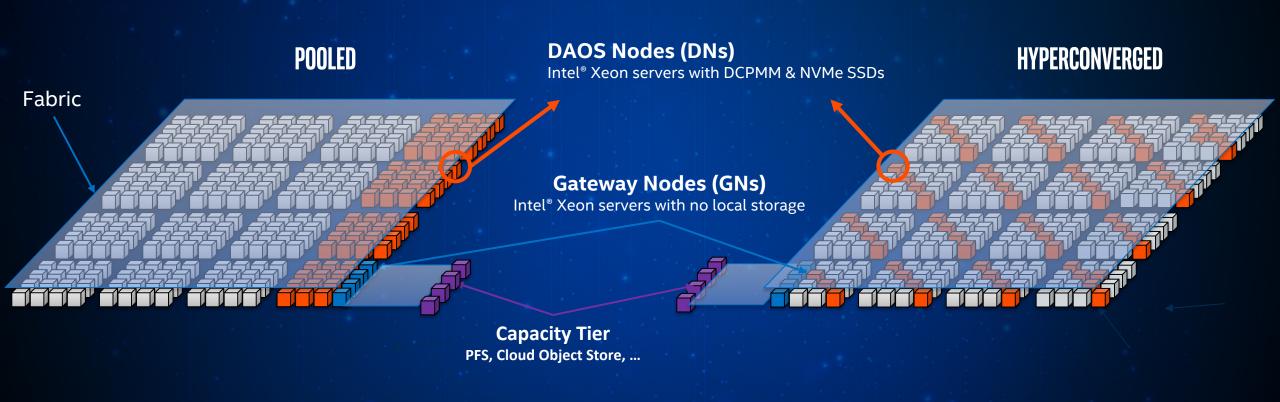


STORAGE ARCHITECTURE





DAOS DEPLOYMENTS





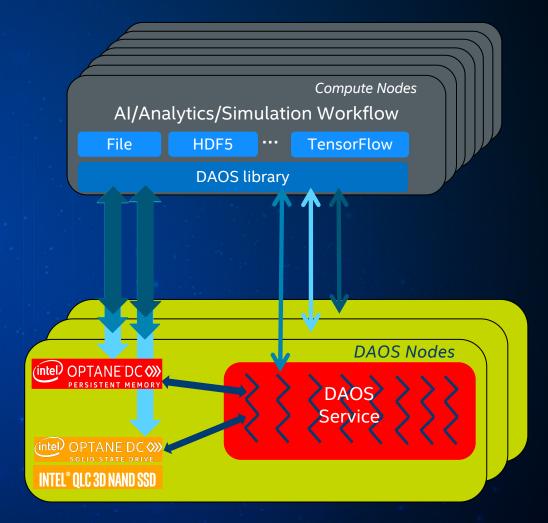
DAOS TIER ANATOMY

DAOS Tier

- Globally accessible from any compute nodes
- Large capacity (100's PB)

DAOS Nodes

- COTS Intel® Xeon servers running the DAOS service
- RNIC attached for communications
 - Support multiple RNICs per server to sustain backend storage IOPS/bandwidth
- Mix of storage technologies attached
 - Intel® Optane™ DC Persistent Memory (DCPMM)
 - NVMe SSD (*NAND, Intel® Optane™ SSDs)



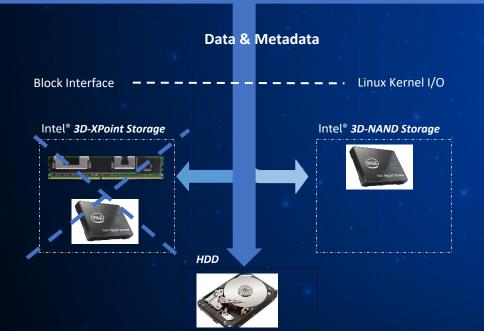


DAOS ARCHITECTURE



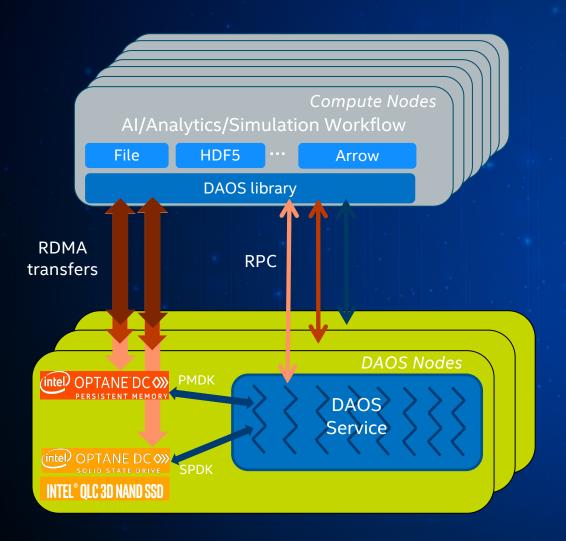
Low-latency high-message-rate communications Collective operations & in-storage computing

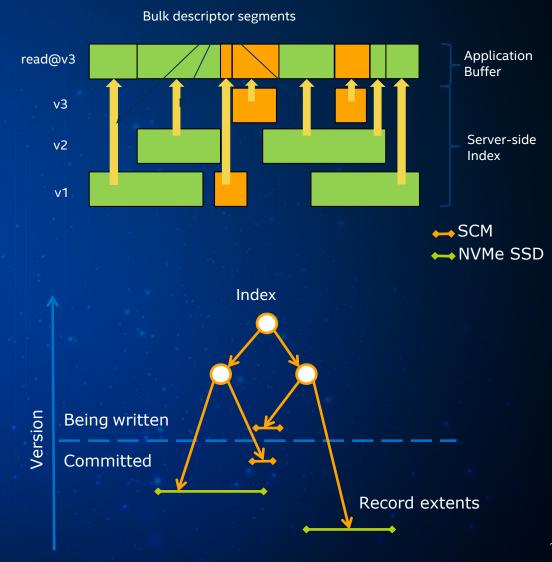
Conventional Storage Systems





LIGHTWEIGHT I/O STACK & FINE-GRAINED I/O





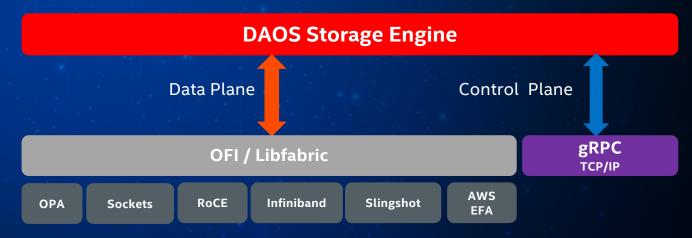
NETWORK SUPPORT

Performance-critical I/O path over libfabric

- Low-latency messaging
 - End-to-end in userspace
- Native support for RDMA
 - True zero-copy I/O
- Non-blocking
- Scalable collective communications

Out-of-band channel for administration

- Manage hardware, service & pools
- Telemetry & troubleshooting
- Secured with TLS & certificate

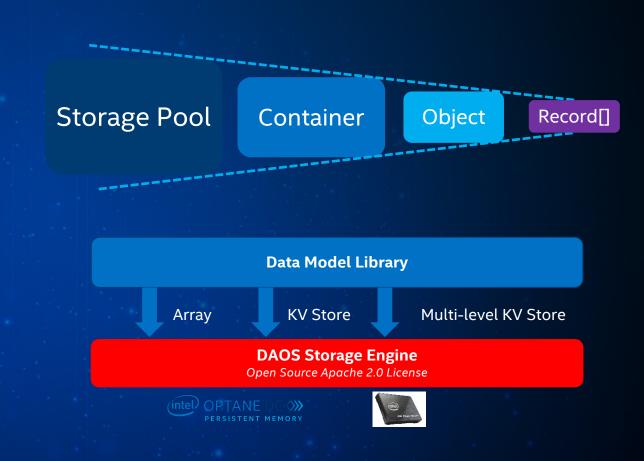




DAOS DATA MODEL

Non-POSIX rich storage API as the new foundation

- Scalable storage model suitable for both structured & unstructured data
 - key-value stores, multi-dimensional arrays, columnar databases, ...
 - Accelerate data analytic/Al frameworks
- Non-blocking data & metadata operations
- Extendable through microservice architecture





STORAGE VIRTUALIZATION & MULTI-TENANCY

Distributed storage reservation

- Intel® Optane ™ DC Persistent Memory (DCPMM)
- NVMe SSD

Predictable capacity

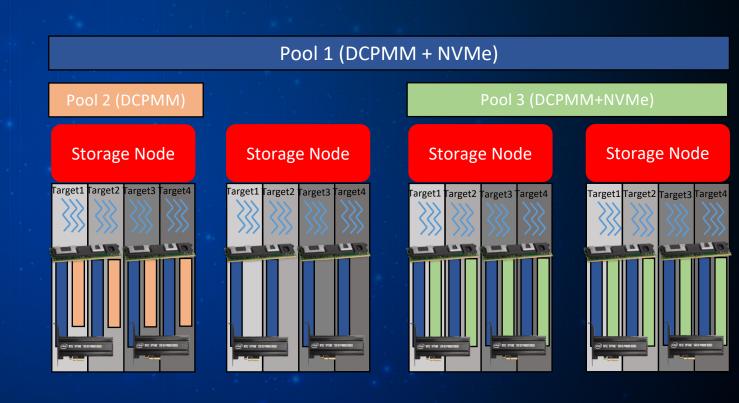
- Can be resized
- Can be extended to span more servers

Multi-tenancy

NFSv4-type ACLs

Typically 1 pool = 1 project

- Can have a single pool or 100's
- Can be ephemeral (per-job) or persistent

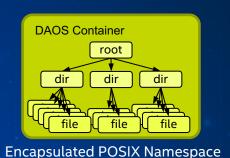


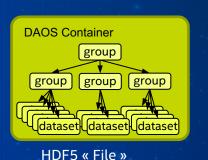


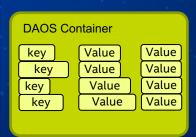
DATASET MANAGEMENT

Aggregate related datasets into manageable and coherent entities

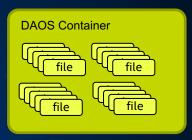
- Distributed consistency & automated recovery
- Full Versioning
- Simplified data management
 - Snapshot
 - Cross-tier Migration
 - Indexing



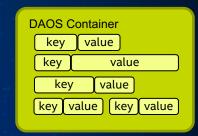




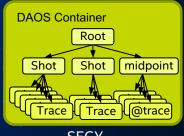




File-per-process



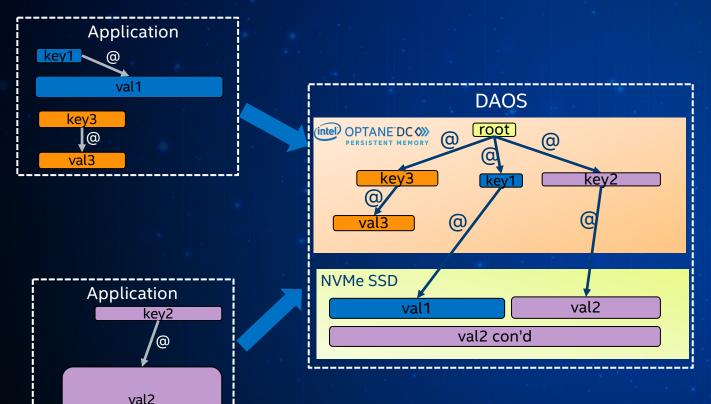
Key-value store



SEGY



ADVANCED STORAGE API



Fast data retrieval

- Avoid file serialization and offset management
- Keys can be of any size/type
- Keys can be ordered with range query support

Scalable insert

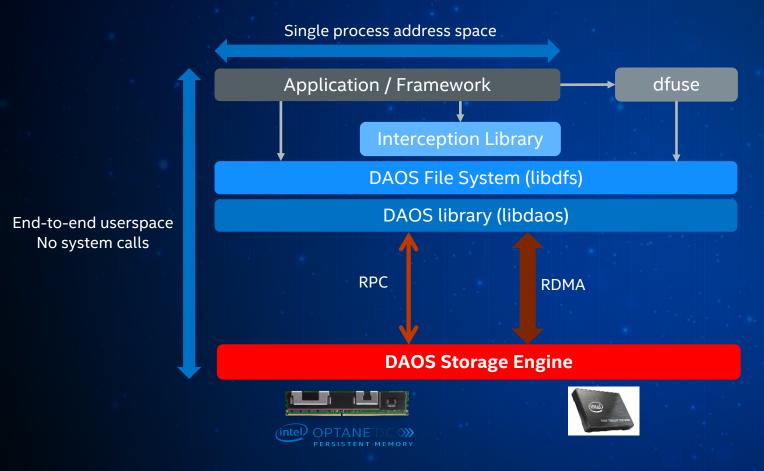
- Allow concurrent access/update
- Distributed transactions keep KV store always consistent

Data indexing

- Query & custom index
- Data provenance



POSIX I/O SUPPORT



DAOS File System (libdfs)

- Encapsulated POSIX namespace
- Application/framework can link directly with libdfs
 - ior/mdtest backend provided
 - MPI-IO driver leveraging collective open
 - TensorFlow, ...

FUSE Daemon (dfuse)

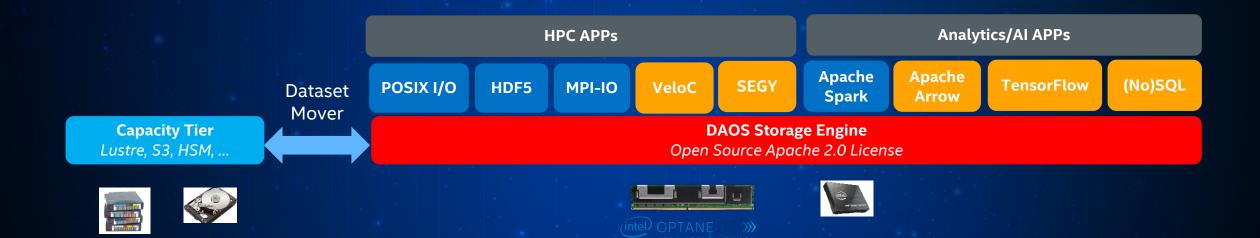
- Transparent access to DAOS
- Involves system calls

I/O interception library

OS bypass for read/write operations



APPLICATION INTERFACE



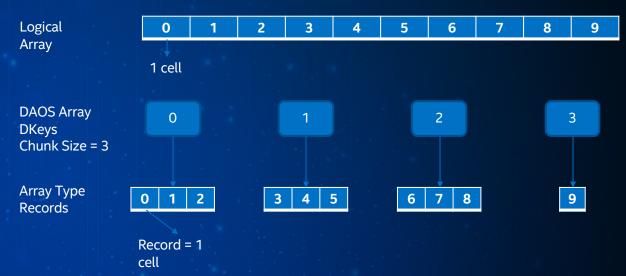


MPI-IO DRIVER FOR DAOS

The DAOS MPI-IO driver is implemented within the I/O library in MPICH (ROMIO).

- Added as an ADIO driver
- Portable to Open-MPI, Intel MPI, etc.
- https://github.com/daos-stack/mpich
- daos_adio branch
- PR to mpich master in review

1 MPI File = 1 DAOS Array Object

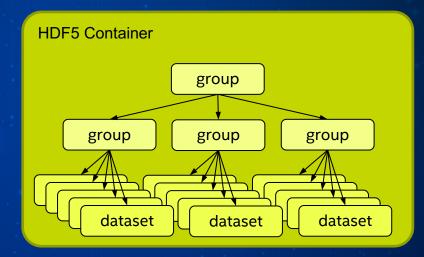


Application works seamlessly otherwise by just specifying the use of the driver by appending "daos:" to the path.



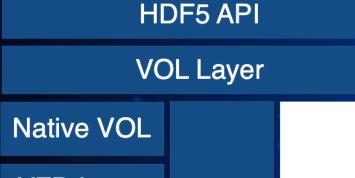
HDF5

- Developing an HDF5 VOL Connector
 - Prototyped in ESSIO
- All applications or middleware I/O libraries (e.g. NetCDF4, PIO, etc.) that use HDF5 would be able to run over the DAOS stack with minimal changes.



Adding new extensions to HDF5 that are not available to date without the DAOS VOL connector

- Asynchronous I/O for both metadata and raw data operations
- Container Snapshots
- Query & Indexing API



VFD Layer REST

SEC2 MPIO HDFS

Storage

Cloud

SCM/ SSD

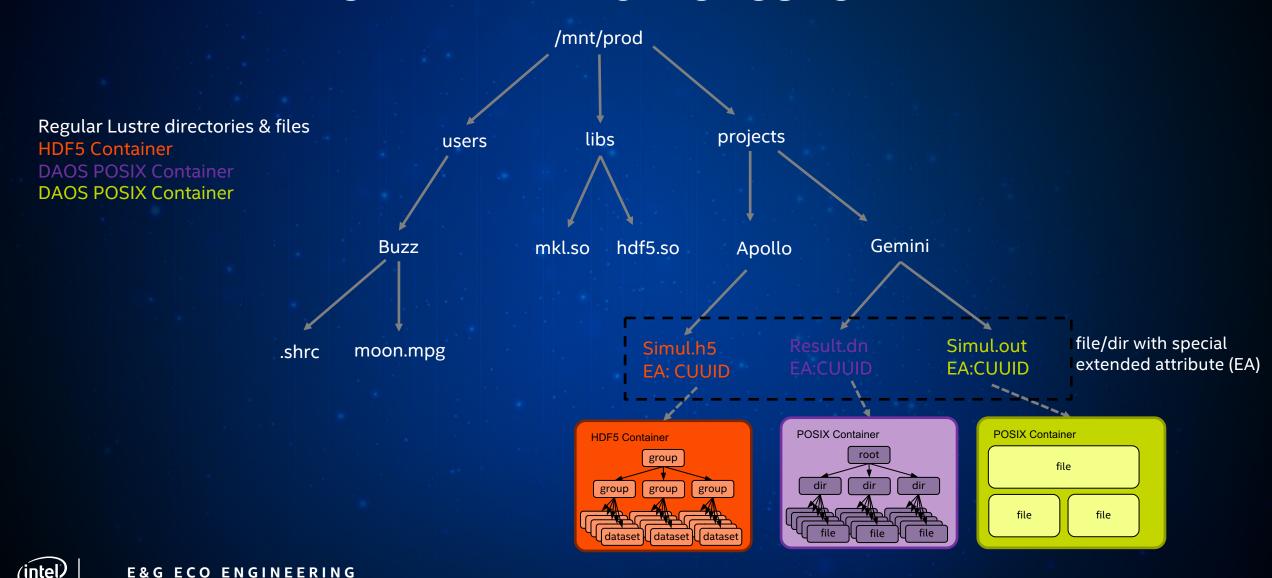
DAOS

VOL



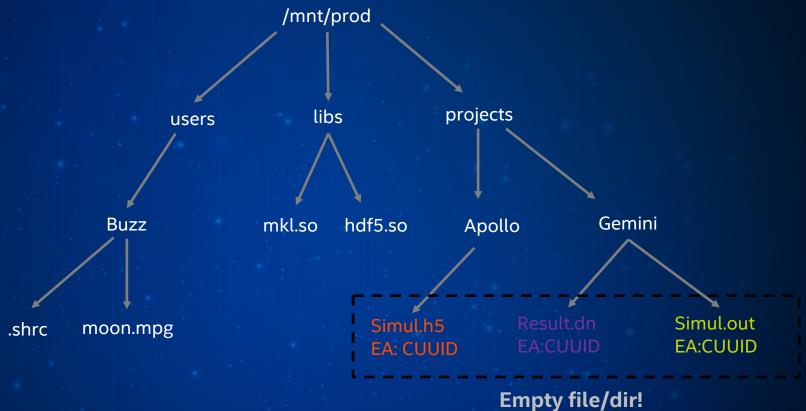
VOL

UNIFIED NAMESPACE CONCEPT

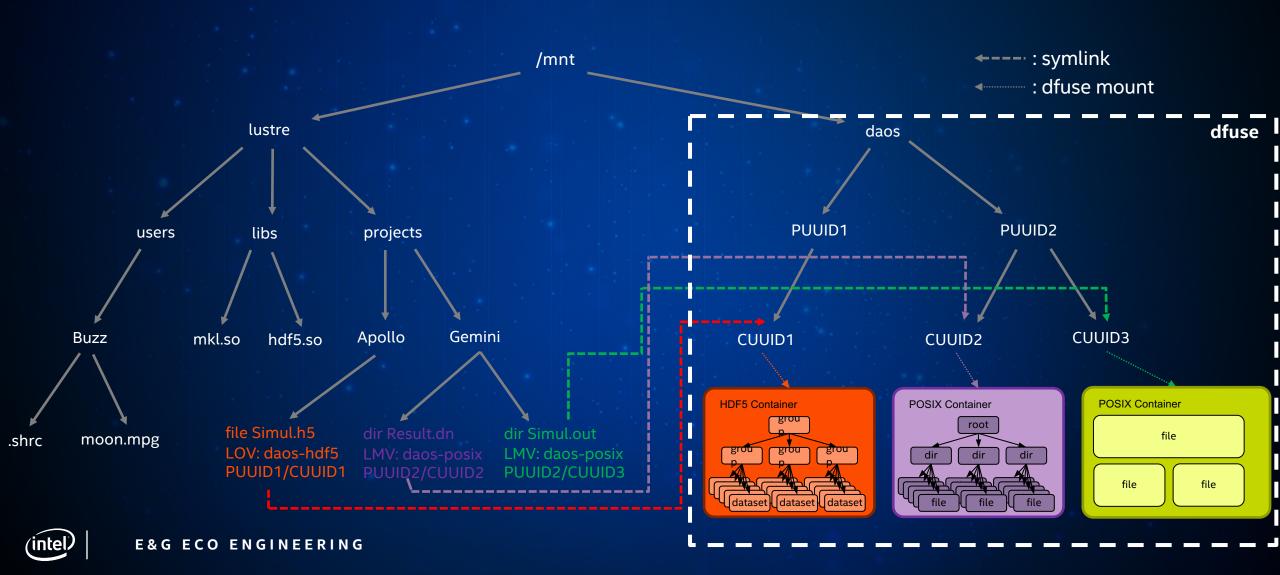


UNIFIED NAMESPACE CONCEPT

Regular Lustre directories & files **HDF5 Container DAOS POSIX Container DAOS POSIX Container**



TRANSPARENT ACCESS OF DAOS STORAGE FROM LUSTRE



Apollo Simul.h5 **HDF5** Container **POSIX Container** group root dataset dataset bar2 bar3 bar1 Mover **Apollo** Result.dn Simul.h5 bar3 bar1 bar2 E&G ECO ENGINEERING

DATA MOVER



- Different use cases
 - POSIX container migration
 - Other middleware specific data migration (e.g. HDF5)
 - **Cross-Pool Container Migration**
- Develop an MPI application
 - Parallel movement of datasets between tiers.
- Provide a library and DAOS tool that allows integration with other data movement frameworks (e.g. Globus, DMF, etc.).

DAOS: PRIMARY STORAGE FOR AURORA



Aurora DAOS configuration

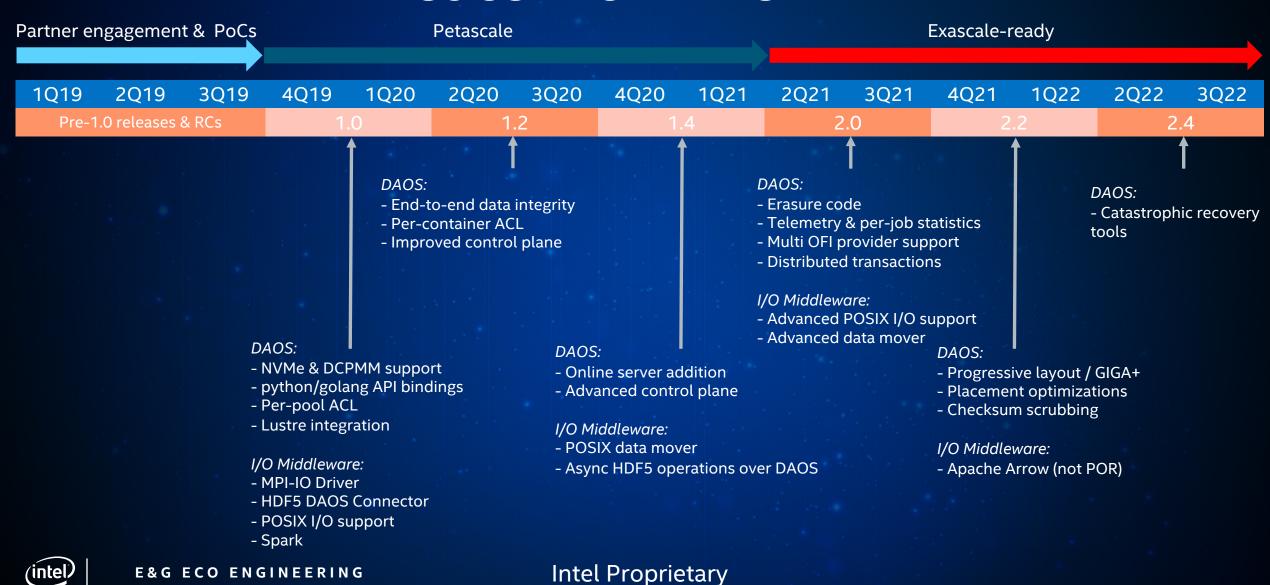
- Capacity: 230PB
- Bandwidth >25TB/s

"The Argonne Leadership Computing Facility will be the first major production deployment of the DAOS storage system as part of Aurora, the first US exascale system coming in 2021. The DAOS storage system is designed to provide the levels of metadata operation rates and bandwidth required for I/O extensive workloads on an exascale-level machine."

Susan Coghlan, ALCF-X Project Director/Exascale Computing Systems Deputy Director



DAOS COMMUNITY ROADMAP



All information provided in this roadmap is subject to change without notice.

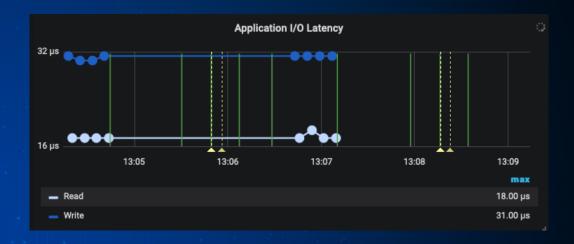
PERFORMANCE

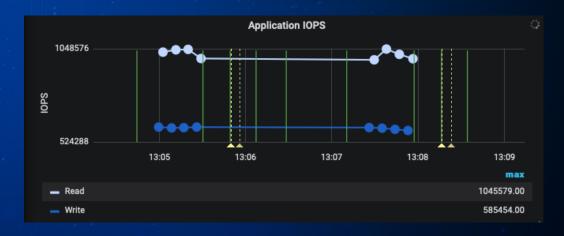
Demonstrated at ISC (1/2U server)

- https://www.youtube.com/watch?v=EMGBcvnftwQ
- https://www.youtube.com/watch?v=e69Rgz2FMbE

Deliver HW performance

- Saturate SSD bandwidth with large blocks
- Latency/IOPS of persistent memory for metadata & small I/Os
- Only need a few clients to reach max performance
 - One task enough to reach 8GB+/s







DAOS RESOURCES

Source code on GitHub

https://github.com/daos-stack/daos

Documentation

http://daos.io

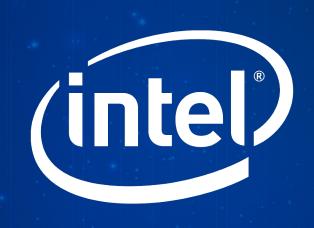
Community mailing list on Groups.io

daos@daos.groups.io

Bug tracker & support

https://jira.hpdd.intel.com





INTEL® OPTANE™ TECHNOLOGY





 Intel® Optane™
 375GB

 DC D4800X
 750GB

 PCle* 3.0 2x2, NVMe*
 1.5TB



Intel® Optane™ DC Persistent Memory

128GB 256GB 512GB

*Other names and brands may be claimed as the property of others.

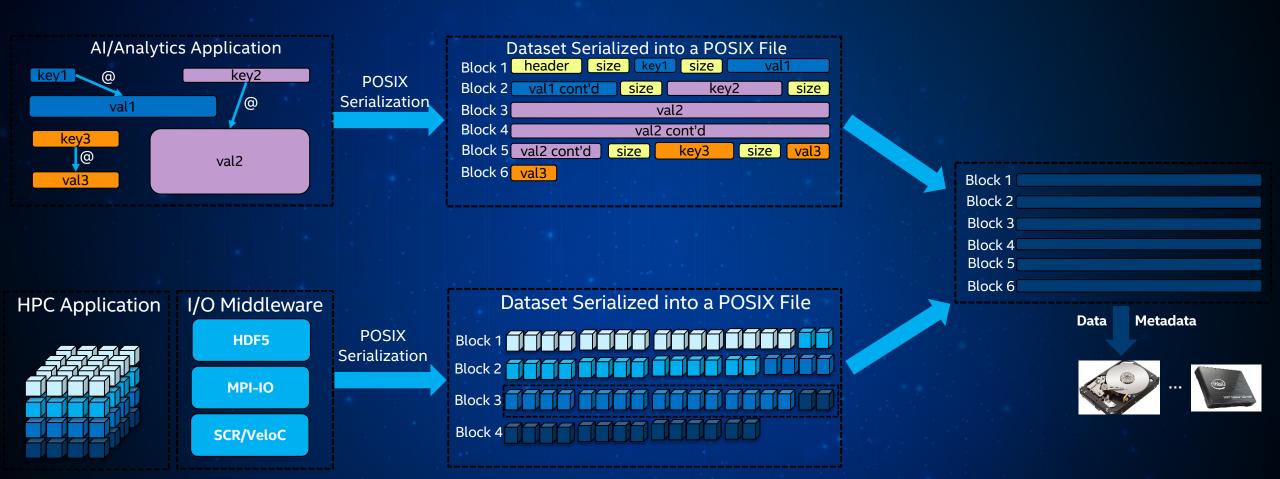
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1.5TB

PCle* 3.0 x4, NVMe*

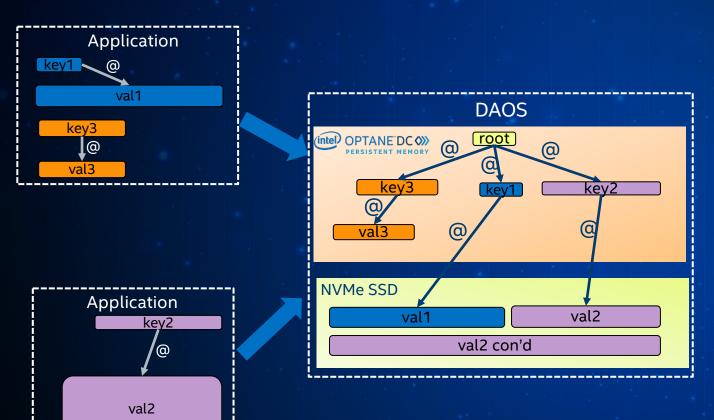


POSIX LIMITATIONS



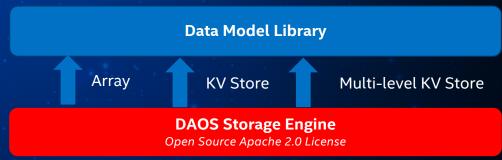


ADVANCED STORAGE API



Native support for structured, semi-structured & unstructured data models

- Built on top of DCPMM
- Unconstrained by POSIX serialization
- Custom attributes
- Data access time orders of magnitude faster (µs)
- Scalable concurrent updates & high IOPS
- Non-blocking
- Enable in-storage computing



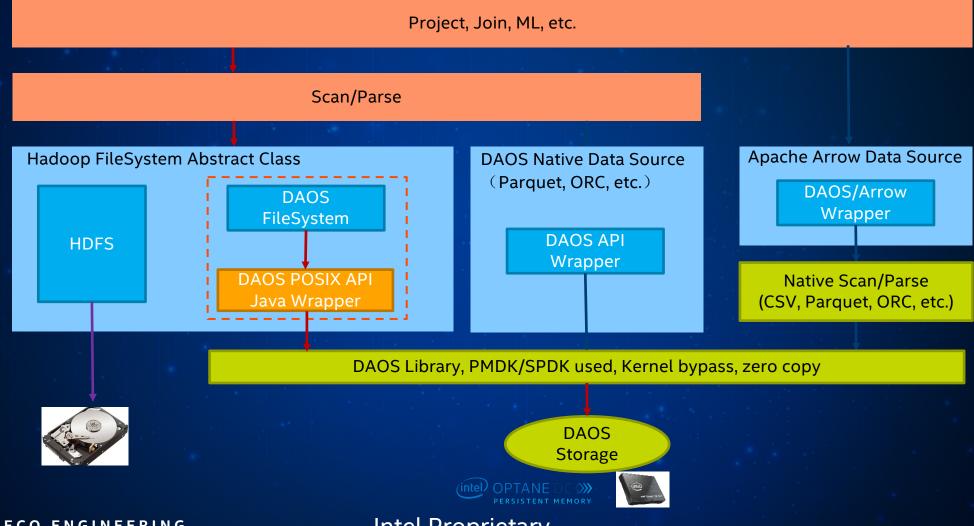






DAOS & BIG DATA / AI







DAOS PROJECT HISTORY



Prototype over Lustre*

Standalone prototype

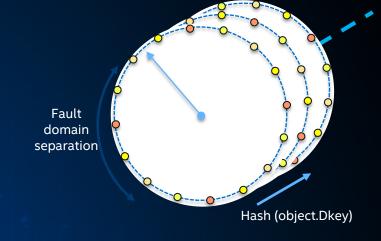
- OS-bypass
- Persistent memory
- Replication & self healing

DAOS Productization

*Other names and brands may be claimed as the property of others.



DATA MANAGEMENT



Data Distribution

- Algorithmic placement
- Progressive layout with GIGA+

Data Protection

- Declustered replication & erasure code
- Fault-domain aware placement
- Self-healing
- End-to-end data integrity

Data Versioning

- Non-destructive write & consistent read
- Native snapshot support

Data Security & Reduction (not POR)

- Online real-time data encryption & compression
- Hardware acceleration



CONTROL PLANE

Storage provisioning

- Detect SCM & NVMe storage
 - CPU/storage affinity
- Configure/format/mount SCM
 - Interleaved mode
- Configure NVMe SSDs
 - Firmware update
- Integrated storage burn-in capability

Fabric configuration

- Comm layer configuration
- Interface/CPU affinity

DAOS configuration

- zero-conf/auto-conf with device filters/manual-conf
- YAML configuration for admins

DAOS service management

- Manage/monitor/troubleshoot
- Integration with systemd & other frameworks

Telemetry

- Storage/service/fabric activity
- Per-job statistics

Storage API & tools

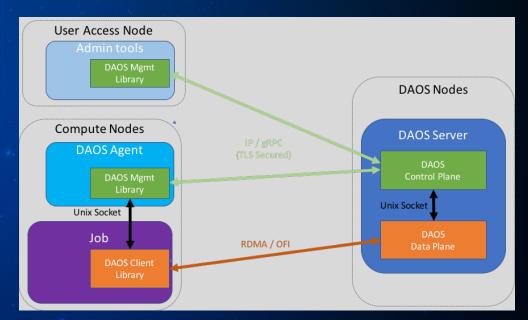
CLI tools built over the control plane API



SECURITY

Flexible security framework

- Support different authentication methods
 - Local agent on compute node authenticating process through AUTH_SYS
 - Third party authentication service (e.g. munge)
- TLS-secured channel using certificates
- Very minimal impact expected on I/O path

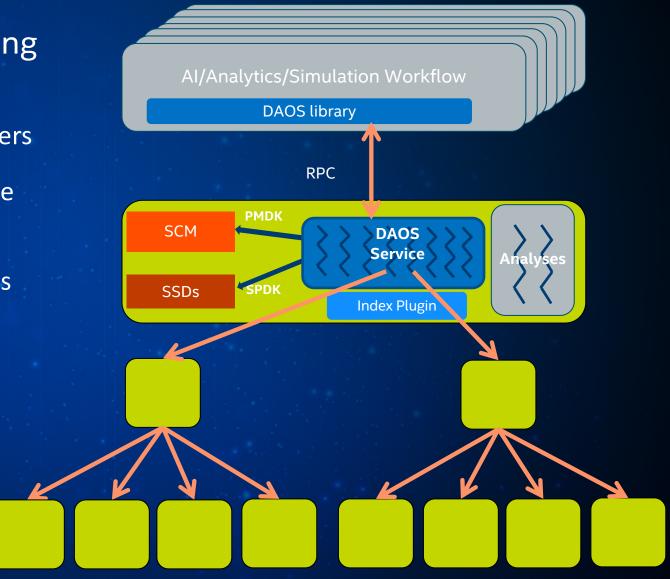




IN-STORAGE COMPUTING

Function shipping for in-situ processing

- Execute pre-defined / user-defined data processing function directly on storage servers
- Prevent loading entire dataset onto compute nodes
- Execute filtering/MapReduce-like operations where data is located
 - Collective with reply aggregation
- Send results back to caller
- Not POR





STORAGE ACCELERATION FRAMEWORK

Offload API for client and server

- ISA-L (software) on IA
- Accelerators (hardware)
 - Intel QuickAssist
 - GPGPU
 - FPGA/SmartNICs (libfabric extensions)



Use cases

- Erasure code
- Checksums
- Compression
- Encryption
- Data indexing/query
- Data transformation
 - Dropping floating point precision
 - AoS to SoA and vice-versa

