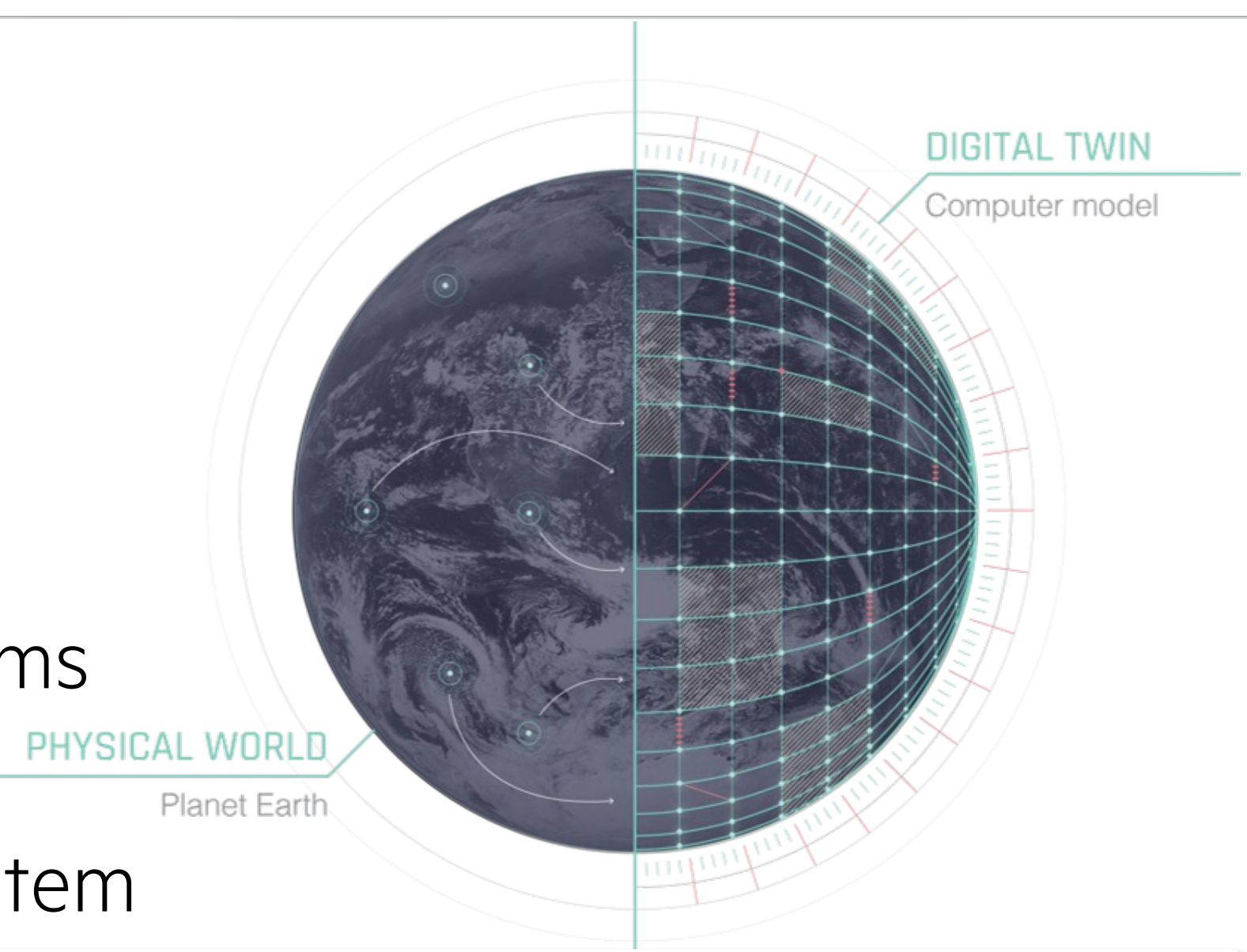


Utz-Uwe Haus, Christopher Haine, Ali Mohammed, Sebastien Cabaniols
HPE HPC/AI EMEA RESEARCH LAB (ERL)

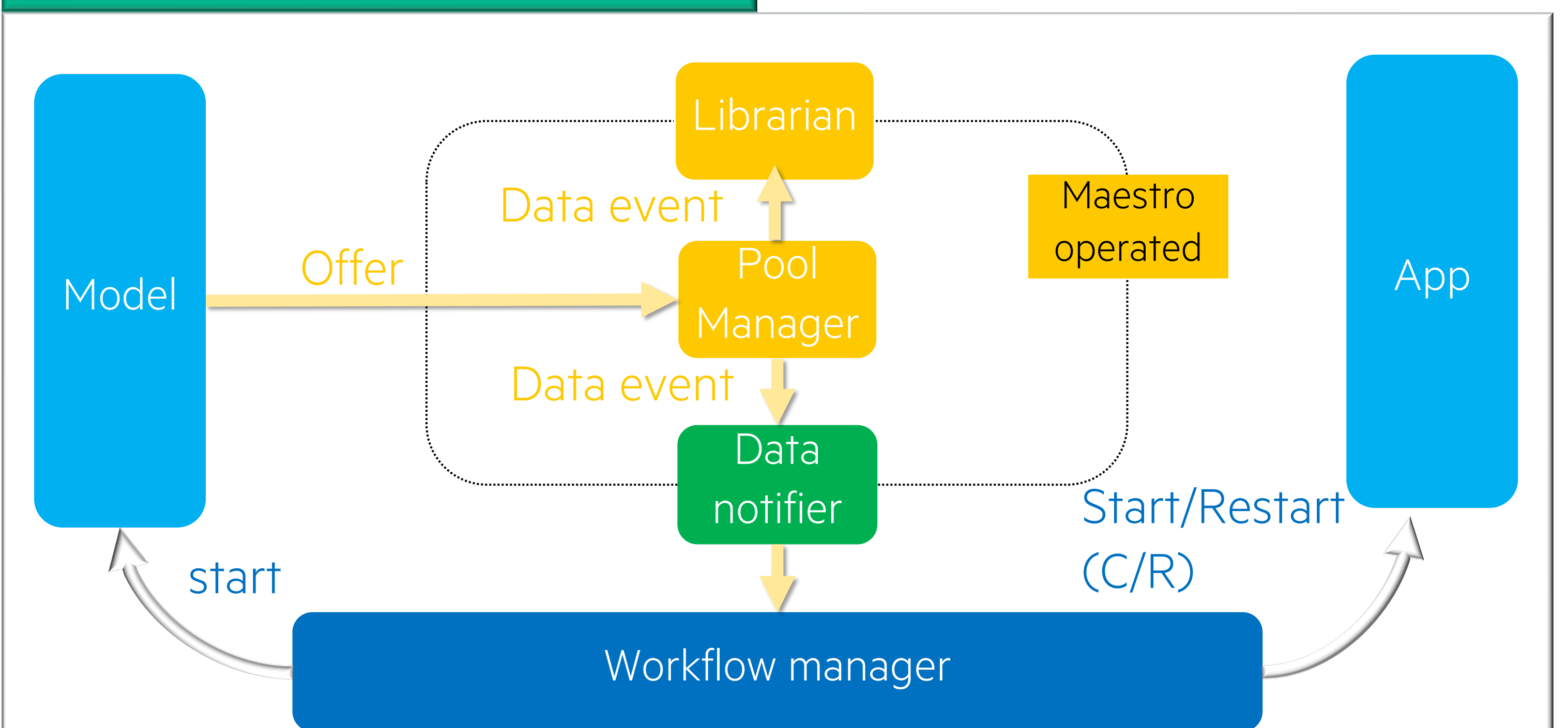
Digital Twins

- (L1) Descriptive
 - retroactive analysis
- (L2) Informative:
 - monitoring & simulation
- (L3) Predictive:
 - predict evolution of systems
- (L4) Living:
 - self-adapts to the real system



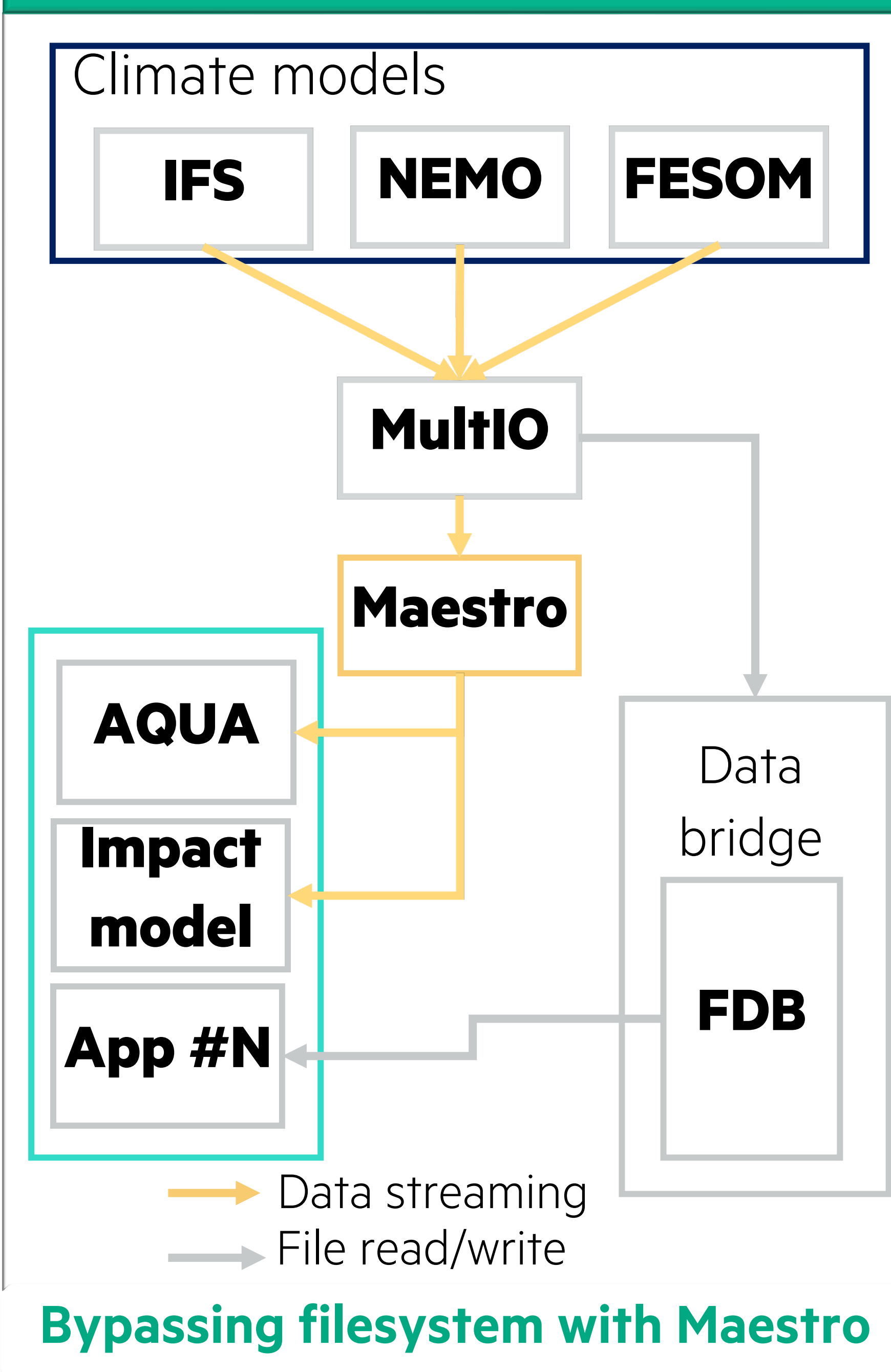
Digital twins exacerbates the data orchestration problem in HPC

Coupling with Workflow

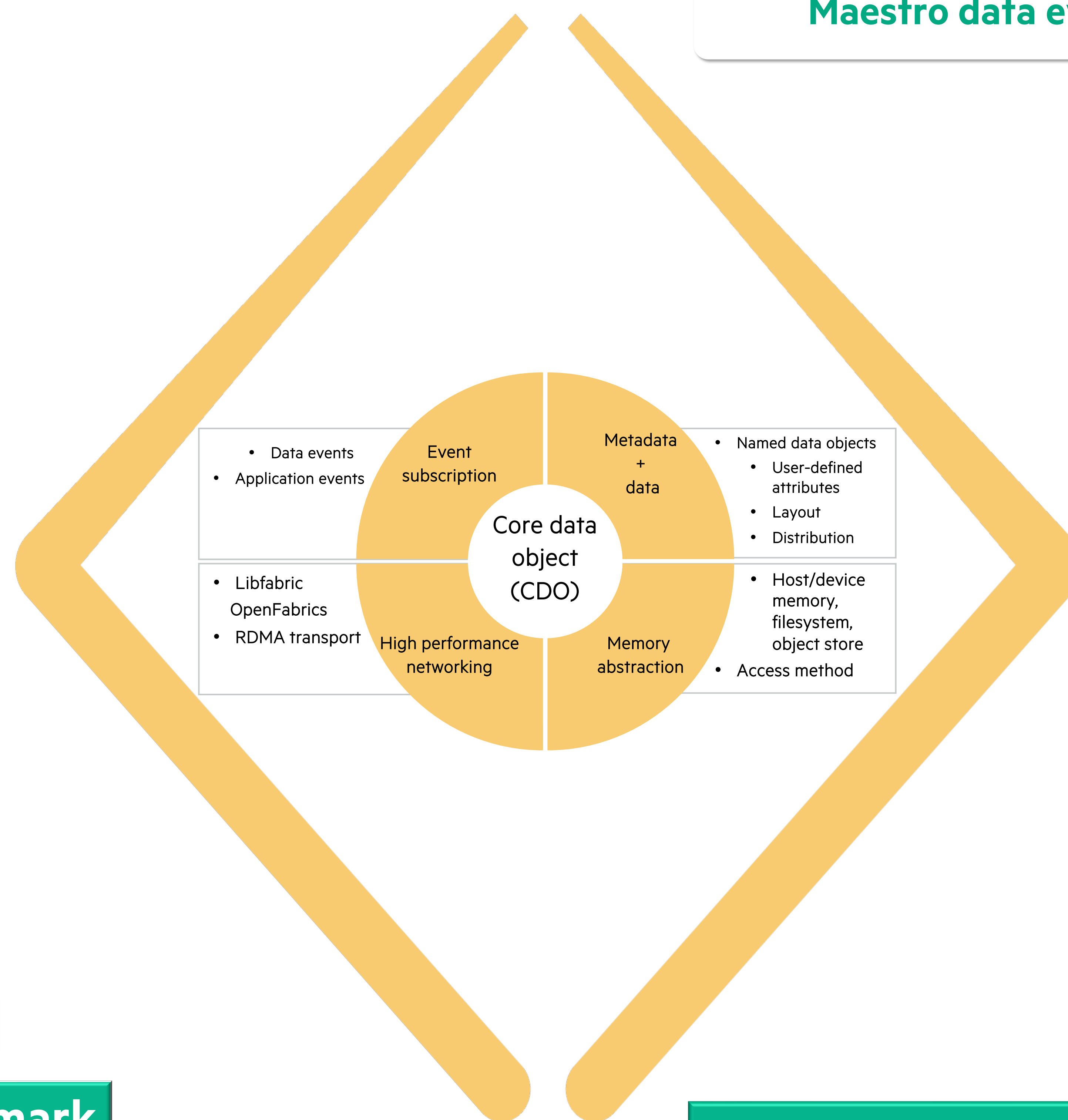


Maestro data events drives the workflow execution

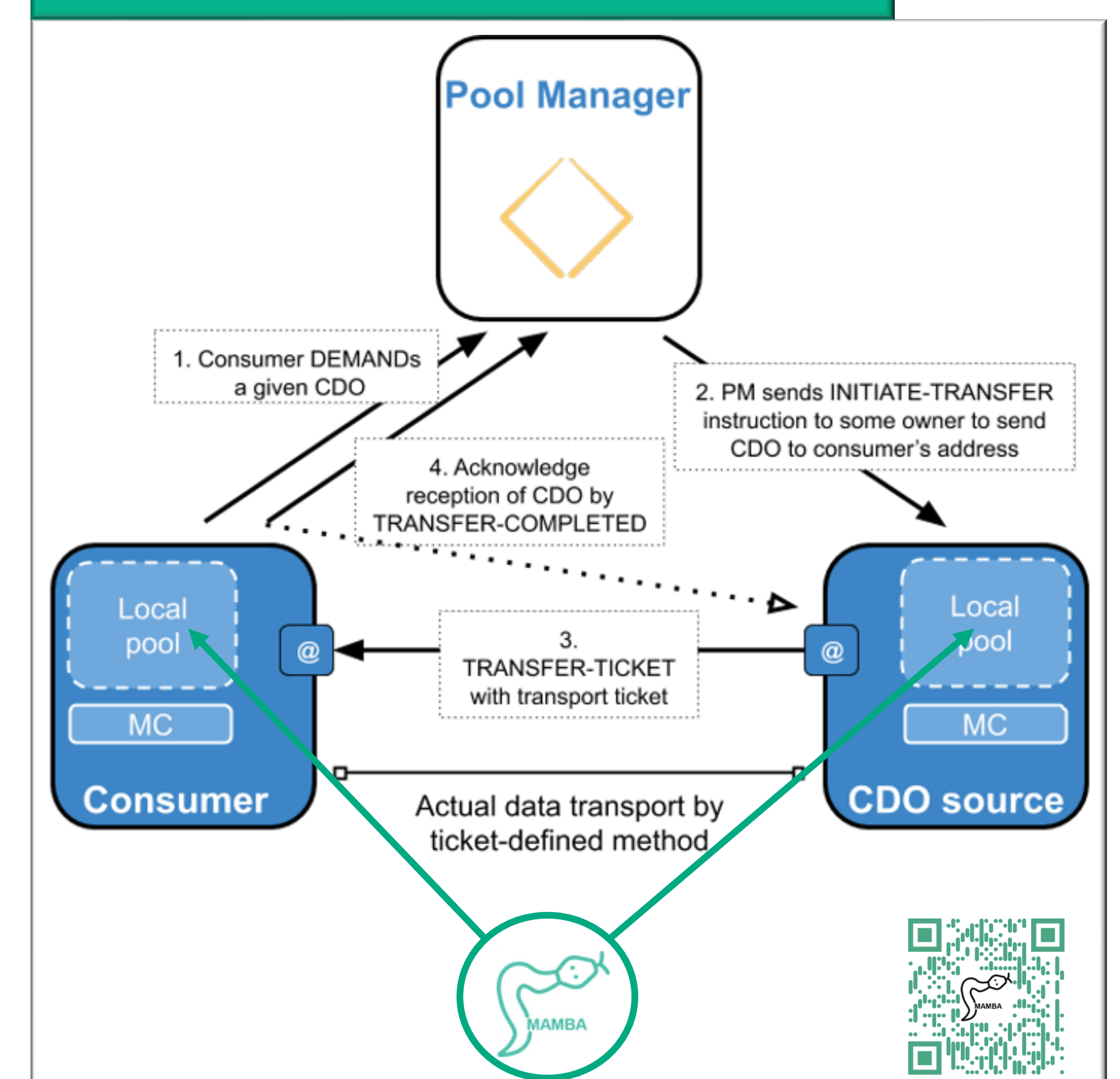
Climate Adaptation Twin



Bypassing filesystem with Maestro

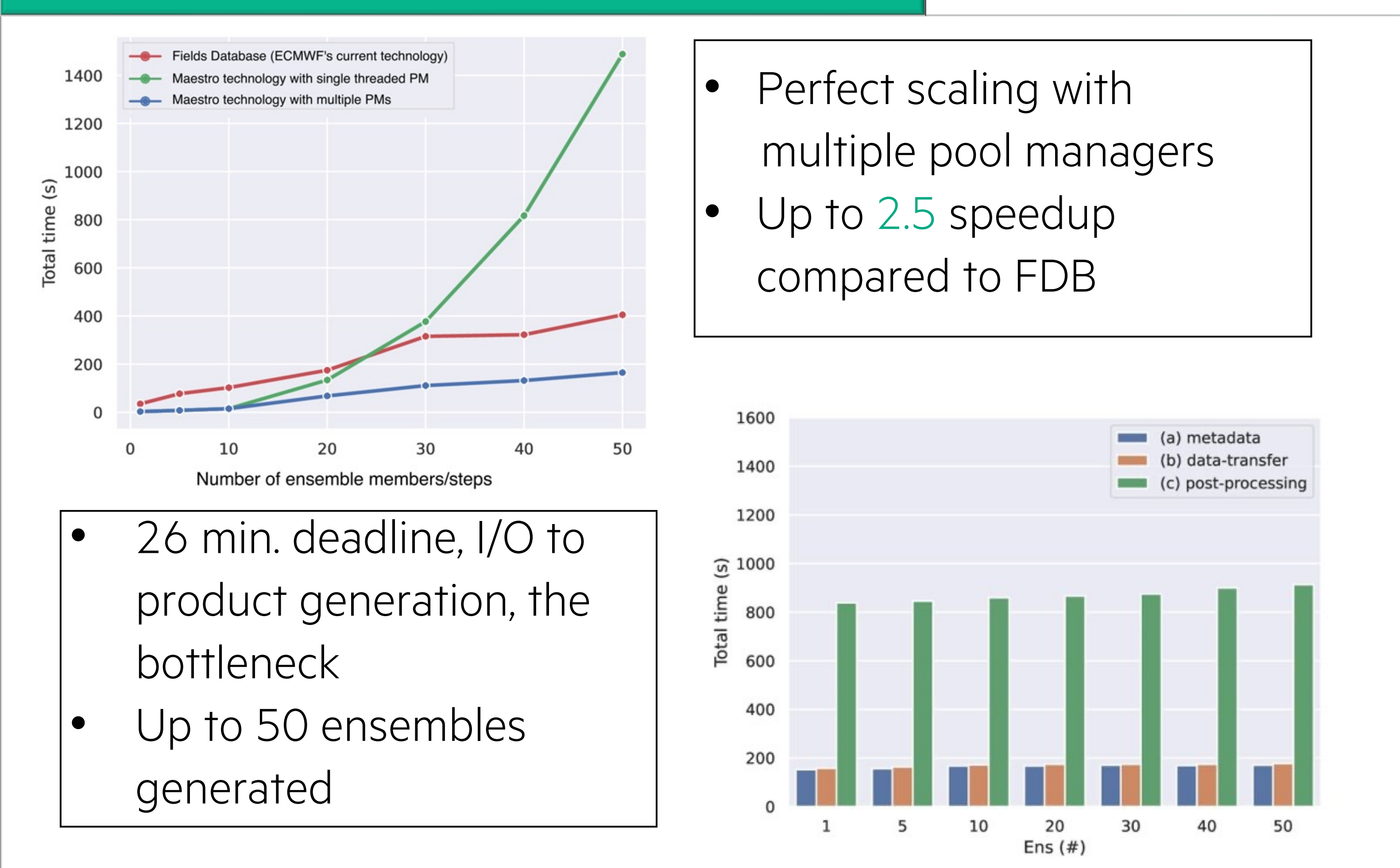


Peer-to-Peer Transport



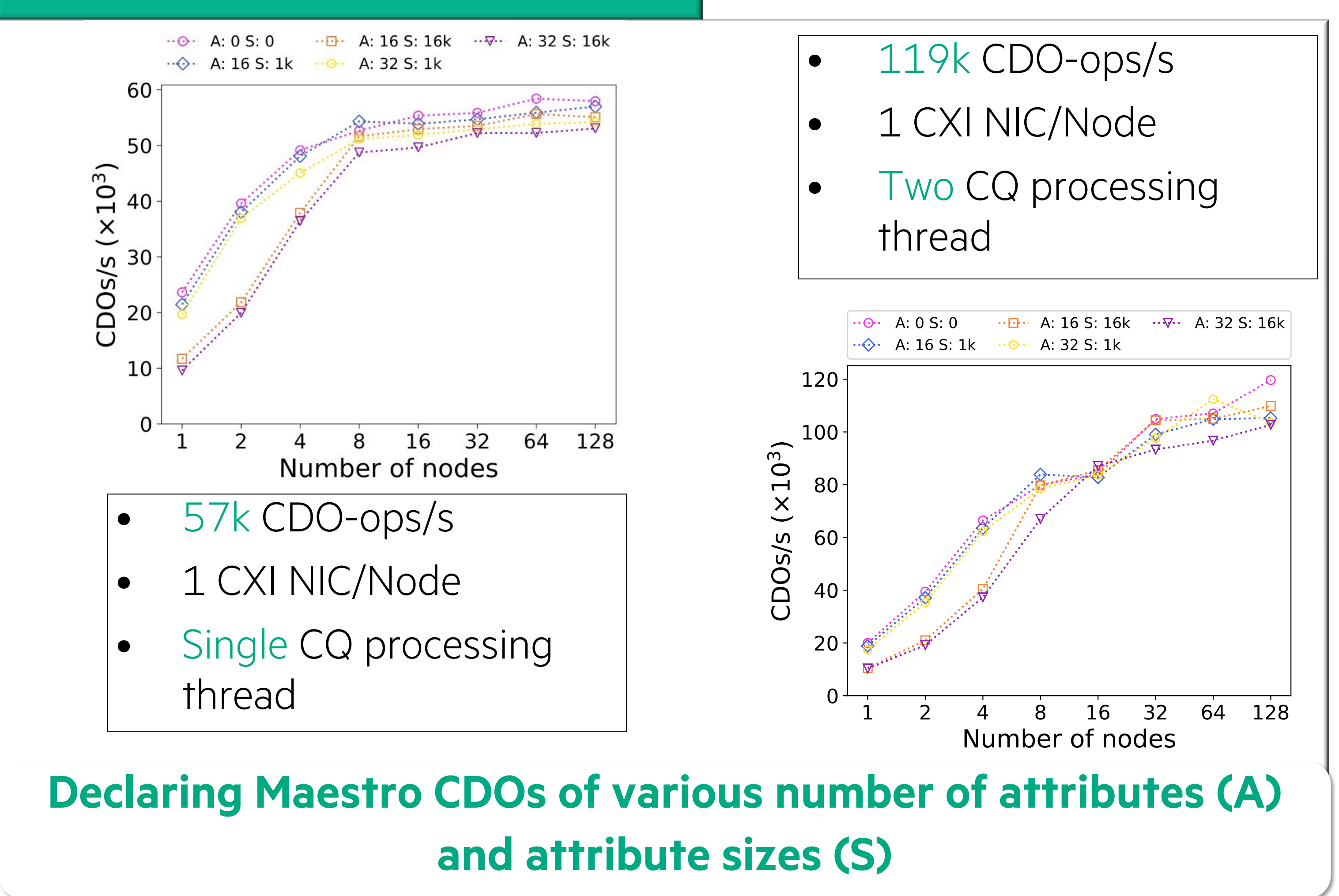
Direct RDMA between components

IFS Product Generation Benchmark



- Perfect scaling with multiple pool managers
- Up to 2.5 speedup compared to FDB

Maestro-core Benchmark



- 119k CDO-ops/s
- 1 CXI NIC/Node
- Two CQ processing thread

Declaring Maestro CDOs of various number of attributes (A) and attribute sizes (S)

<https://www.ecmwf.int/en/about/media-centre/science-blog/2022/secondment-ecmwf-maestro-project>